



NOTE:
 A Resistor maybe added externally to any of the bias points except THR_bias to increase or decrease respective bias currents by connecting it to +5v or gnd as the case maybe.
 For example: (Refer to the Chip Bias Circuit)
 To increase V2I_BIAS connect Rext to gnd and to decrease V2I_BIAS connect Rext to V3.
 Similarly, to increase S1_BIAS connect Rext1 to V1 and to decrease S1_BIAS connect Rext to gnd.

NOTE:
 1. Use R6 to increase time between s1 ON and s2 ON to > 300ns
 2. Use R6a to decrease time between s1 ON and s2 ON to < 300ns
 3. Use R7 to increase time between s2 ON and dscrm ON to > 900ns
 4. Use R7 to decrease time between s2 ON and dscrm ON to < 900ns
 5. C8 is critical. C6 and C7 may smooth V2 supply during switching. C9, C11 bypass switching transients on V3. C10 and C12 bypass reset transients on V3. C5 may not do much.